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Express Mail No.:

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. 66638/47062	
In Re Application Of: Shooks et al.				
Serial No. 10/828,675	Filing Date April 21, 2004	Examiner Not yet assigned	Group Art Unit 2874	
Title: Embedded Fiber Optic Demodulator				

Address to:
Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

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Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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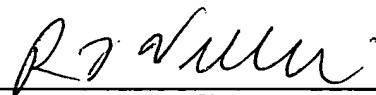
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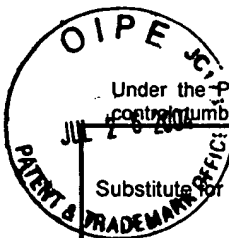

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Dated: July 21, 2004


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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	10/828,675
Filing Date	April 21, 2004
First Named Inventor	Shooks et al.
Art Unit	2874
Examiner Name	Not yet assigned
Attorney Docket Number	66638-47062

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA	US- 6,204,920	Mar. 20, 2001	Ellerbrock et al.	
	AB	US- 4,668,093	May 26, 1987	Cahill	
	AC	US- 4,471,659	Sep. 18, 1984	Udd et al.	
	AD	US- 5,380,995	Jan. 10, 1995	Udd et al.	
	AE	US- 5,563,967	Oct. 8, 1996	Haake	
	AF	US- 5,627,927	May 6, 1997	Udd	
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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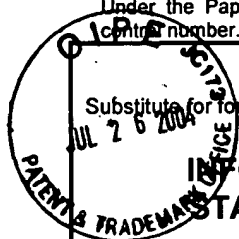
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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/828,675
Filing Date	April 21, 2004
First Named Inventor	Shooks et al.
Group Art Unit	2874
Examiner Name	Not yet assigned
Attorney Docket Number	66638-47062

Sheet	2	of	2
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
AG		UNIVERSITY OF MINNESOTA; <i>Wideband Integrated Acoustic Emission Microsensors</i> ; Paper, undated	
AH		THE BOEING COMPANY; <i>Enhancements to Fiber Optic Bragg Grating Sensors and Demodulation Systems</i> ; article; published by The Boeing Company, St. Louis, MO; printed 7/21/2004	
AI		AXSUN TECHNOLOGIES; <i>Optical Monitor Product Family</i> ; Product Description; Axsun Technologies, Billerica, MA 01821	
AJ		FIBER OPTIC SENSORS AND SENSING SYSTEMS INC.; <i>Company Pamphlet</i> ; Belgium	
AK		AXSUN TECHNOLOGIES; <i>SpectralEye Interrogator Family</i> ; Performance specifications for SpectralEye Models; Copyright 2004	
AL		BORKAR, SHEKHAR Y.; <i>Silicon Photonics: New Opportunities for Silicon</i> ; Intel DeveloperUPDATE Magazine; April 2002, pp. 1-5	
AM		INTEL; <i>Fiber Optic Speeds with Silicon Efficiencies</i> ; www.intel.com/labs/sp/indexx.htm ; 4/1/2004; pp. 1-3	

Examiner Signature		Date Considered	
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